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FILING DATE **APPLICATION NUMBER GRP ART UNIT** FIL FEE REC'D ATTY.DOCKET.NO DRAWINGS TOT CLAIMS IND CLAIMS 09/882,760 06/15/2001 2661 511 Li7 24 6

> **CONFIRMATION NO. 1794 FILING RECEIPT**

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Date Mailed: 08/16/2001

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

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Domestic Priority data as claimed by applicant

THIS APPLN CLAIMS BENEFIT OF 60/212,333 06/16/2000

Foreign Applications

If Required, Foreign Filing License Granted 08/09/2001

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**Early Publication Request: No** 

\*\* SMALL ENTITY \*\*

Title

Packet switch with one-stop buffer in memory with massive parallel access

**Preliminary Class** 

370



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2664



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Bib Data Sheet

**CONFIRMATION NO. 1794** 

<b>SERIAL NUMBER</b> 09/882,760	<b>FILING DATE</b> 06/15/2001 <b>RULE</b>	C	370	GROUP ART UNIT 2664		ATTORNEY DOCKET NO. Li7			
APPLICANTS Shuo-Yen Robe Jian Zhu, Shenz	ert Li, Shatin, HONG KC zhen, CHINA;	NG;							
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** FOREIGN APPLICA	ATIONS ***********	***							
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** SMALL ENTITY ** ** 08/09/2001									
Foreign Priority claimed			STATE OR COUNTRY HONG KONG	SHEETS DRAWING 7		TOT. CLAI 24	MS	INDEPENDENT CLAIMS 6	
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TITLE									
Packet switch with one	e-stop buffer in memory	with ma	ssive parallel a	access					
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